

Electronic Acknowledgement Receipt

EFS ID:	2149347
Application Number:	10757788
International Application Number:	
Confirmation Number:	5462
Title of Invention:	Method and apparatus for the automatic correction of faulty wires in a logic simulation hardware emulator / accelerator
First Named Inventor/Applicant Name:	Thomas Michael Gooding
Customer Number:	30206
Filer:	James R. Nock/Deb Peterson
Filer Authorized By:	James R. Nock
Attorney Docket Number:	ROC920030335US1
Receipt Date:	31-AUG-2007
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Application Type:	Utility under 35 USC 111(a)

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Submitted with Payment	yes
Payment was successfully received in RAM	\$450
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes) /Message Digest	Multi Part /zip	Pages (if appl.)
1	Extension of Time	ROC920030335US1_ExtensionofTime_08-31-07.pdf	20115 e5a65e808fe8fc4073694673d1f8772ba102811d	no	2
Warnings:					
Information:					
2	Fee Worksheet (PTO-06)	fee-info.pdf	8212 a7390b5315d5a530cc0401583e6d936017ae48b7	no	2
Warnings:					
Information:					
Total Files Size (in bytes):				28327	
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